



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 4479

SERIAL NUMBER 10/670,194	FILING DATE 09/26/2003 RULE	CLASS 257	GROUP ART UNIT 2811	ATTORNEY DOCKET NO. 1035-471
-----------------------------	---------------------------------------	--------------	------------------------	---------------------------------

APPLICANTS

Toshio Kimura, Kitakatsuragi-gun, JAPAN;
 Yoshihisa Dotta, Nara-shi, JAPAN;

** CONTINUING DATA ***** *none Jmz*

** FOREIGN APPLICATIONS ***** *Jmz*
 JAPAN 2002-313528 10/28/2002

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
 ** 12/29/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 32	TOTAL CLAIMS 18	INDEPENDENT CLAIMS 2
Verified and Acknowledged	<i>Puyefu Jm</i> Examiner's Signature	<i>Jmz</i> Initials			

ADDRESS
 23117
 NIXON & VANDERHYE, PC
 901 NORTH GLEBE ROAD, 11TH FLOOR
 ARLINGTON, VA
 22203

TITLE
 Chip-stack semiconductor device and manufacturing method of the same

FILING FEE RECEIVED 750	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
----------------------------	---	---